

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,906,961 B2  
APPLICATION NO. : 10/602991  
DATED : June 14, 2005  
INVENTOR(S) : Eggleston et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**In the Drawings:**

At sheet 9 of 12, delete Figure 6B and replace with attached Replacement Figure 6B.

Column 8, Line 63, delete "other erase block 302, 308 of the erase block pair 300. For"  
and insert --other erase block 302, 304 of the erase block pair 300. For--

Column 9, Line 35, delete "written to the user data area 230 of Sector 0 228 of Erase"  
and insert --written to the user data area 230 of Sector 0 234 of Erase--

Column 9, Line 47, delete "the user data area 230 of Sector 0 228 of Erase Block 1 226"  
and insert --the user data area 230 of Sector 0 234 of Erase Block 1 226--

Column 11, Line 35, delete "this manner until all the user data areas 406 of the sectors"  
and insert --this manner until all the user data areas 456 of the sectors--

Column 11, Line 45, delete "read from the super block 400 starting with the user data  
area" and insert --read from the super block 450 starting with the user data area--

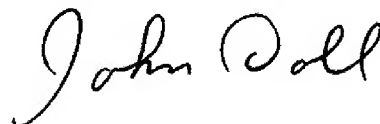
Column 11, Line 51, delete "is then written to/read from the user data area 410 of the  
next" and insert --is then written to/read from the user data area 460 of the next--

Column 13, Line 54, delete "contains four ECC codes 410 that correspond to the four"  
and insert --contains four ECC codes 610 that correspond to the four--

Column 16, Line 50, delete "control circuit 812 is coupled to read 816 the sector data"  
and insert --control circuit 812 is coupled to read 822 the sector data--

Signed and Sealed this

Seventh Day of July, 2009



JOHN DOLL  
*Acting Director of the United States Patent and Trademark Office*

U.S. Patent

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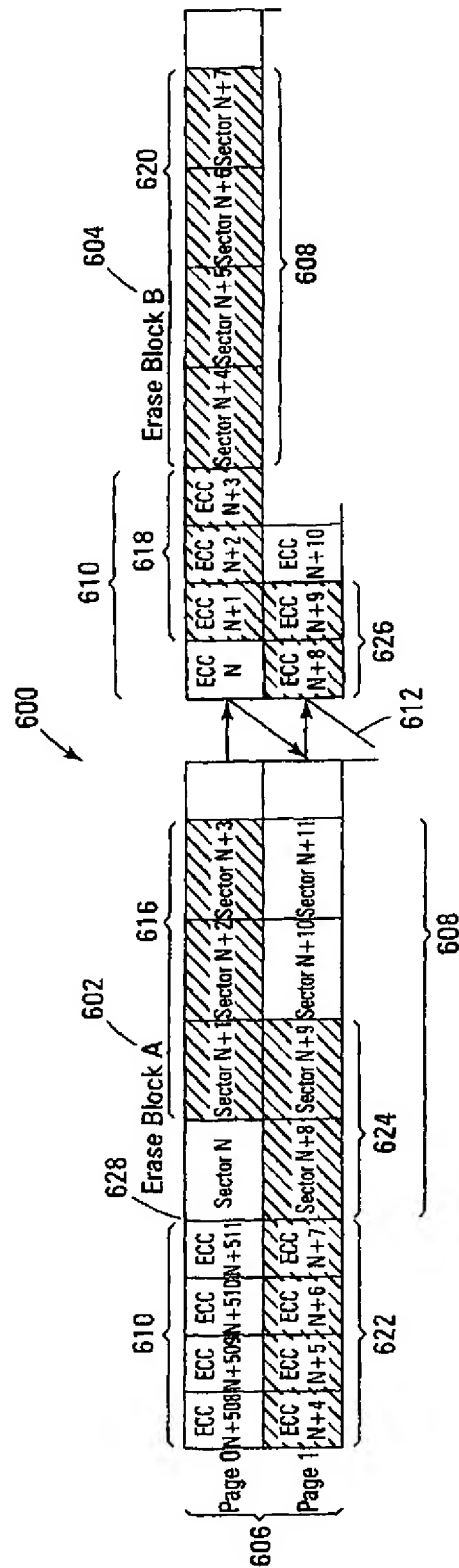


Fig. 6B